#### **REMARKS**

Reconsideration and further examination of the present application is respectfully requested.

# Acknowledgement of Domestic Priority Claim

The present application claims priority from U.S. Application No. 08/998,583, filed December 29, 1997.

Applicant notes, however, that box 15 of the Office Action Summary in the Office Action has not been checked to acknowledge this domestic priority claim.

Applicant therefore respectfully requests acknowledgement of this domestic priority claim in the next Office Action.

# Objection to the Drawings

In the Office Action, the drawings are objected to for various reasons.

Applicant respectfully submits herewith under separate cover proposed drawing corrections in red for original Figures 1, 3-5, 7-10, and 12.

Applicant respectfully submits this objection has been overcome and should accordingly be withdrawn.

#### Objection to the Abstract

In the Office Action, the Abstract is objected to.

Applicant respectfully submits this objection has been overcome by the amendment to the abstract in this Response to Office Action. Applicant therefore respectfully submits this objection should accordingly be withdrawn.

### Rejections Under 35 U.S.C. §§ 102(e), 103(a)

In the Office Action, claims 1-9, 13-21, 25-28, 31-37, and 40-44 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,038,625 to Ogino et al. ("Ogino"), and claims 10-12, 22-24, 29-30, 38-39, and 45-46 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Ogino.

Applicant respectfully traverses these rejections as follows.

The present application is a continuation-in-part patent application of U.S. Application No. 08/998,583, filed December 29, 1997. Applicant respectfully submits independent claims 1, 13, 25, 33, and 40 are each supported by this parent application. Because the parent application was filed December 29, 1997 and therefore prior to the January 6, 1998 filing date of Ogino, Applicant respectfully submits Ogino cannot be used to reject these claims.

Claim 1 is supported by the parent application, for example, in Figure 1, on page 8 at lines 6-11, on page 9 at lines 10-12 and 18-20, and on page 10 at lines 1-2 and 20-23.

Claim 13 is supported by the parent application, for example, in Figure 1, on page 8 at lines 6-11, on page 9 at lines 10-12 and 18-20, and on page 10 at lines 1-2 and 20-23.

Claim 25 is supported by the parent application, for example, on page 8 at lines 6-11, on page 9 at lines 10-12 and 18-20, on page 10 at lines 11-18, on page 10 at line 23 through page 11 at line 14.

Claim 33 is supported by the parent application, for example, in Figure 2 and on page 11 at line 16 through page 12 at line 2.

Claim 40 is supported by the parent application, for example, in Figure 2, on page 10 at lines 11-18, and on page 11 at line 16 through page 12 at line 2.

Noting claims 2-12, 14-24, 26-32, 34-39, and 41-46 each depend from claim 1, 13, 25, 33, or 40, Applicant therefore respectfully submits these rejections have been overcome and should accordingly be withdrawn.

**MARKED UP VERSION OF AMENDMENTS** 

**IN THE ABSTRACT** 

On page 26 of the application, the paragraph starting on line 2 has been amended as

follows.

[The invention, in one particular embodiment is a] A bus system [, comprising] comprises

a bus, a first bus device on the bus at a first virtual address and at a first physical address on the

bus, and a second bus device on the bus at a second virtual address and a second physical

address. The bus system further comprises a map of the first and second virtual addresses to the

first and second physical addresses, respectively, encoded on a program storage medium. [, the]

The map [being] is accessible over the bus. The first and second virtual addresses may each be,

for example, a guaranteed unique identifier (GUID).

IN THE CLAIMS

3. (Amended Once) The bus system of claim 1, [where in] wherein the map resides on at

least one of the first and second bus devices.

5. (Amended Once) The bus system of claim 4, wherein the bus manager comprises [one

of] a workstation [and] or a personal computer.

8. (Amended Once) The bus system of claim 1, wherein at least one of the first and second

bus devices is [selected from the group comprising] a printer, a plotter, a workstation, a personal

computer, a video camera, [and] or a magnetic tape drive.

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- 9. (Amended Once) The bus system of claim 1, wherein the map is encoded as [a structure selected from the group of] an array, a doubly linked list, a tree, a table, [and] or a file.
- 13. (Amended Once) A [dynamically configurable] bus system, comprising:
  - a dynamically configurable bus; and
- a plurality of bus devices coupled to the bus, each [one] of the plurality of bus devices having a virtual address and a physical address, at least one of the plurality of bus devices mapping at least one virtual address to the respective physical address for the bus device associated with the respective physical address.
- 14. (Amended Once) The bus system of claim 13, wherein at least one [of the first and second virtual addresses] <u>virtual address</u> is a guaranteed unique identifier.
- 15. (Amended Once) The bus system of claim 13, wherein [the] a map resides on at least one of the [first and second] bus devices.
- 16. (Amended Once) The bus system of claim 13, wherein at least one of the [first and second] bus devices is a bus manager.
- 17. (Amended Once) The bus system of claim 16, wherein the bus manager comprises [one of] a workstation [and] or a personal computer.

- 18. (Amended Once) The bus system of claim 16, wherein [the] a map is stored on the bus manager.
- 20. (Amended Once) The bus system of claim 13, wherein at least one of the [first and second] bus devices is [selected from the group comprising] a printer, a plotter, a workstation, a personal computer, a video camera, [and] or a magnetic tape drive.
- 21. (Amended Once) The bus system of claim 13, wherein [the] a map is encoded as [a structure selected from the group of] an array, a doubly linked list, a tree, a table, [and] or a file.
- 22. (Amended Once) The bus system of claim 13, wherein [the] a map is bi-directional.
- 25. (Amended Once) A program storage device encoded with instructions that, when executed by a computer, map a plurality of virtual addresses to [the] respective physical addresses for a plurality of bus devices in a dynamically configurable bus system upon detecting a configuration event.
- 26. (Amended Once) The program storage device of claim 25, wherein each virtual address and [the] its respective physical address is mapped into at least one of an array, a doubly linked list, a tree, a table, and a file.

27. (Amended Once) The program storage device of claim 25, wherein the mapping of a plurality of virtual addresses to [the] respective physical addresses includes:

querying each [one] of the plurality of [the] bus devices other than [the] <u>a</u> bus manager; identifying each queried device from [the] configuration information [therefore] therefor;

[ascertain] <u>ascertaining</u> the virtual address and the physical address for each identified device;

constructing a map of the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor; and storing the map.

- 29. (Amended Once) The program storage device of claim 25, wherein the dynamically configurable bus system comprises a first dynamically configurable bus and a second dynamically configurable bus and the [act of] mapping a plurality of virtual addresses to [the] respective physical addresses is performed only for [the] bus devices on [the particular] one of the first and second dynamically configurable buses experiencing a configuration event.
- 30. (Amended Once) The program storage device of claim 25, wherein the encoded instructions, when executed, map the virtual addresses to [the] respective physical addresses bidirectionally.
- 31. (Amended Once) The program storage device of claim 25, wherein [the] at least one mapped virtual address is a guaranteed unique identifier.

33. (Amended Once) A method [for robust addressing on a bus including a plurality of bus devices, one of the bus devices being a bus manager, the method] comprising:

querying each [one] of [the] <u>a</u> plurality of [the] bus devices other than [the] <u>a</u> bus manager;

identifying each queried device from [the] configuration information [therefore] therefor; ascertaining [the] a virtual address and [the] a physical address for each identified device; mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor; and storing [the] a map.

- 34. (Amended Once) The method of claim 33, wherein the querying each [one] of the plurality of <u>bus</u> devices includes querying at least one of a printer, a plotter, a workstation, a personal computer, a video camera, and a magnetic tape drive.
- 35. (Amended Once) The method of claim 33, wherein the bus manager comprises [one of] a workstation [and] or a personal computer.
- 36. (Amended Once) The method of claim 33, wherein the storing the map includes storing the map on the bus manager.

37. (Amended Once) The method of claim 33, wherein the mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor includes mapping each virtual address and [the] its respective physical address into at least one of an array, a

doubly linked list, a tree, a table, and a file.

- 38. (Amended Once) The method of claim 33, wherein the mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor includes bidirectionally mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor.
- 39. (Amended Once) The method of claim 33, wherein the [dynamically configurable bus comprises a first dynamically configurable bus and a second dynamically configurable bus and the act] querying is performed only for [the] bus devices on [the particular one of the] a first [and] or second dynamically configurable [buses] bus experiencing a configuration event.
- 40. (Amended Once) A program storage device encoded with instructions that, when executed by a computer, perform [the] a method comprising:
- querying each [one] of [the] <u>a</u> plurality of [the] bus devices other than [the] <u>a</u> bus manager;

identifying each queried device from [the] configuration information [therefore] therefor; [ascertain] ascertaining [the] a virtual address and [the] a physical address for each identified device;

mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor; and storing [the] a map.

- 41. (Amended Once) The program storage device of claim 40, wherein the querying each [one] of the plurality of <u>bus</u> devices [in the method] includes querying at least one of a printer, a plotter, a workstation, a personal computer, a video camera, and a magnetic tape drive.
- 42. (Amended Once) The program storage device of claim 40, wherein the bus manager comprises [one of] a workstation [and] or a personal computer.
- 43. (Amended Once) The program storage device of claim 40, wherein the map [in the method] includes storing the map on the bus manager.
- 44. (Amended Once) The program storage device of claim 40, wherein the mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor [in the method] includes mapping each virtual address and [the] its respective physical address into at least one of an array, a doubly linked list, a tree, a table, and a file.
- 45. (Amended Once) The program storage device of claim 40, wherein <u>the</u> mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] <u>therefor</u>

[in the method] includes bi-directionally mapping the virtual address for each of the plurality of bus devices to the physical address [therefore] therefor.

46. (Amended Once) The [method] <u>program storage device of claim 40</u>, wherein the [dynamically configurable bus comprises a first dynamically configurable bus and a second dynamically configurable bus and the act] querying is performed only for [the] bus devices on [the particular one of the] <u>a</u> first [and] <u>or</u> second dynamically configurable [buses] <u>bus</u> experiencing a configuration event.

Applicant respectfully submits the present application is in condition for allowance, for which early action is earnestly solicited.

The Examiner is invited to telephone the undersigned to help expedite any further prosecution of the present application.

The Director of the U.S. Patent and Trademark Office is hereby authorized to credit any overpayment or to charge any fees or fee deficiencies under 37 C.F.R. §§ 1.16 and 1.17 in connection with this communication to our Deposit Account No. 02-2666.

Respectfully submitted,

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Date: September 18, 2001

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